

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	61331	(state adj2 machine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 20:32
S2	1764	((state adj2 machine) with model\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:07
S3	36	((state adj2 machine) with model\$4) fork\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 20:38
S4	23	((state adj2 machine) with model\$4) non-determin\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 20:38
S5	1	("2003/0046609").URPN.	USPAT	OR	OFF	2007/02/18 20:41
S6	15	("5099440" "5187788" "5331579" "5495409" "5541863" "5671415" "5826065" "5995753" "6101524" "6158001" "6173438" "6289502" "6405361" "6408262").PN. OR ("6880147").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/02/18 20:56
S7	1	"5659555".pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/02/18 20:58
S8	12	("5555270" "5623499" "5630051" "5659555" "5703885" "5796752" "6004027" "6282621" "20030159087" "20030191797" "7088864" "7149678").pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/02/18 21:00
S9	71	((state adj2 machine) with model\$4 with check\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:07

EAST Search History

S10	13	((state adj2 machine) with model\$4 with check\$4) (non-determinis\$ or nondeterminis\$)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:20
S11	2120	703/2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:20
S12	112	703/2.ccls. (state adj2 machine)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:20
S13	46	703/2.ccls. (state adj2 machine) with model\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:21
S14	9	703/2.ccls. (state adj2 machine) with model\$4 (nondeterminis\$ or (non-determinis\$))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:24
S15	3	326/46.ccls. (state adj2 machine) with model\$4 (nondeterminis\$ or (non-determinis\$))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:26
S16	3	714/21.ccls. (state adj2 machine) with model\$4 (nondeterminis\$ or (non-determinis\$))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2007/02/18 21:26



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» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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View: 1-

- ☐ 1. **Algebras of discrete event models**
Inan, K.M.; Varaiya, P.P.;
[Proceedings of the IEEE](#)
Volume 77, Issue 1, Jan. 1989 Page(s):24 - 38
Digital Object Identifier 10.1109/5.21068
[AbstractPlus](#) | Full Text: [PDF\(1020 KB\)](#) IEEE JNL
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- ☐ 2. **On the Bounds for State-Set Size in the Proofs of Equivalence Between D Nondeterministic, and Two-Way Finite Automata**
Moore, F.R.;
[Computers, IEEE Transactions on](#)
Volume C-20, Issue 10, Oct. 1971 Page(s):1211 - 1214
[AbstractPlus](#) | Full Text: [PDF\(960 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 3. **Modular Networks and Nondeterministic Sequential Machines**
Drilman, J.; Weiner, P.;
[Computers, IEEE Transactions on](#)
Volume C-21, Issue 10, Oct. 1972 Page(s):1124 - 1129
[AbstractPlus](#) | Full Text: [PDF\(1128 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 4. **Delayed Universal Logic Modules and Sequential Machine Synthesis**
Le Van, T.; Van Houtte, N.;
[Computers, IEEE Transactions on](#)
Volume C-24, Issue 8, Aug. 1975 Page(s):853 - 855
[AbstractPlus](#) | Full Text: [PDF\(512 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 5. **A note on deciding controllability in pushdown systems**
Griffin, C.;
[Automatic Control, IEEE Transactions on](#)
Volume 51, Issue 2, Feb. 2006 Page(s):334 - 337
Digital Object Identifier 10.1109/TAC.2005.863513
[AbstractPlus](#) | Full Text: [PDF\(176 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **6. Starvation and critical race analyzers for Ada**
Karam, G.M.; Buhr, R.J.A.;
[Software Engineering, IEEE Transactions on](#)
Volume 16, Issue 8, Aug. 1990 Page(s):829 - 843
Digital Object Identifier 10.1109/32.57622
[AbstractPlus](#) | [Full Text: PDF\(1392 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **7. Regular sets and rank order processors**
Butz, A.R.;
[Acoustics, Speech, and Signal Processing \[see also IEEE Transactions on Sig](#)
[IEEE Transactions on](#)
Volume 38, Issue 2, Feb. 1990 Page(s):241 - 246
Digital Object Identifier 10.1109/29.103059
[AbstractPlus](#) | [Full Text: PDF\(572 KB\)](#) IEEE JNL
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- ☐ **8. Abstractions of finite-state machines and immediately-detectable output**
Oikonomou, K.N.;
[Computers, IEEE Transactions on](#)
Volume 41, Issue 3, March 1992 Page(s):325 - 338
Digital Object Identifier 10.1109/12.127444
[AbstractPlus](#) | [Full Text: PDF\(972 KB\)](#) IEEE JNL
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- ☐ **9. Test selection based on communicating nondeterministic finite-state machines generalized Wp-method**
Gang Luo; von Bochmann, G.; Petrenko, A.;
[Software Engineering, IEEE Transactions on](#)
Volume 20, Issue 2, Feb. 1994 Page(s):149 - 162
Digital Object Identifier 10.1109/32.265636
[AbstractPlus](#) | [Full Text: PDF\(1088 KB\)](#) IEEE JNL
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- ☐ **10. Abstractions of finite-state machines and optimality with respect to immediately detectable next-state faults**
Oikonomou, K.N.;
[Systems, Man and Cybernetics, Part A, IEEE Transactions on](#)
Volume 26, Issue 1, Jan. 1996 Page(s):151 - 160
Digital Object Identifier 10.1109/3468.477870
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(960 KB\)](#) IEEE JNL
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- ☐ **11. The state reduction of nondeterministic finite-state machines**
Damiani, M.;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction](#)
Volume 16, Issue 11, Nov. 1997 Page(s):1278 - 1291
Digital Object Identifier 10.1109/43.663818
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(388 KB\)](#) IEEE JNL
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- ☐ **12. Theory and algorithms for state minimization of nondeterministic FSMs**
Kam, T.; Villa, T.; Brayton, R.K.; Sangiovanni-Vincentelli, A.L.;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction](#)
Volume 16, Issue 11, Nov. 1997 Page(s):1311 - 1322
Digital Object Identifier 10.1109/43.663820
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(464 KB\)](#) IEEE JNL
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- ☐ **13. Formal verification of digital systems by automatic reduction of data path**
Macii, E.; Plessier, B.; Somenzi, F.;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction](#)
Volume 16, Issue 10, Oct. 1997 Page(s):1136 - 1156
Digital Object Identifier 10.1109/43.662676
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(804 KB\)](#) IEEE JNL
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- ☐ **14. Discrete approximation and supervisory control of continuous systems**
Raisch, J.; O'Young, S.D.;
[Automatic Control, IEEE Transactions on](#)
Volume 43, Issue 4, April 1998 Page(s):569 - 573
Digital Object Identifier 10.1109/9.664160
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(168 KB\)](#) IEEE JNL
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- ☐ **15. Model matching for finite-state machines**
Di Benedetto, M.D.; Sangiovanni-Vincentelli, A.; Villa, T.;
[Automatic Control, IEEE Transactions on](#)
Volume 46, Issue 11, Nov. 2001 Page(s):1726 - 1743
Digital Object Identifier 10.1109/9.964683
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(445 KB\)](#) IEEE JNL
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- ☐ **16. Timed Wp-method: testing real-time systems**
En-Nouaary, A.; Dssouli, R.; Khendek, F.;
[Software Engineering, IEEE Transactions on](#)
Volume 28, Issue 11, Nov. 2002 Page(s):1023 - 1038
Digital Object Identifier 10.1109/TSE.2002.1049402
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(466 KB\)](#) IEEE JNL
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- ☐ **17. Optimal transfer trees and distinguishing trees for testing observable no finite-state machines**
Fan Zhang; To-yat Cheung;
[Software Engineering, IEEE Transactions on](#)
Volume 29, Issue 1, Jan. 2003 Page(s):1 - 14
Digital Object Identifier 10.1109/TSE.2003.1166585
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(432 KB\)](#) IEEE JNL
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- ☐ **18. Testing from a nondeterministic finite state machine using adaptive state**
Hierons, R.M.;
[Computers, IEEE Transactions on](#)
Volume 53, Issue 10, Oct. 2004 Page(s):1330 - 1342
Digital Object Identifier 10.1109/TC.2004.85
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(792 KB\)](#) IEEE JNL
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- ☐ **19. Polynomial synthesis of supervisor for partially observed discrete-event allowing nondeterminism in control**
Kumar, R.; Shengbing Jiang; Changyan Zhou; Wenbin Qiu;
[Automatic Control, IEEE Transactions on](#)
Volume 50, Issue 4, April 2005 Page(s):463 - 475
Digital Object Identifier 10.1109/TAC.2005.844725
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(544 KB\)](#) IEEE JNL
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- ☐ **20. A test generation algorithm for systems modelled as non-deterministic F**
AboElfotou, H.; Abou-Rabia, O.; Ural, H.;
[Software Engineering Journal](#)
Volume 8, Issue 4, July 1993 Page(s):184 - 188
[AbstractPlus](#) | [Full Text: PDF\(320 KB\)](#) IET JNL
- ☐ **21. Stimulus generation for interface protocol verification using the nondeter**
extended finite state machine model
Che-Hua Shih; Juinn-Dar Huang; Jing-Yang Jou;
[High-Level Design Validation and Test Workshop, 2005. Tenth IEEE Internatic](#)
30 Nov.-2 Dec. 2005 Page(s):87 - 93
Digital Object Identifier 10.1109/HLDVT.2005.1568819
[AbstractPlus](#) | [Full Text: PDF\(256 KB\)](#) IEEE CNF
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- ☐ **22. Discovering effective strategies for the iterated prisoner's dilemma using**
algorithms
Glomba, M.; Filak, T.; Kwasnicka, H.;
[Intelligent Systems Design and Applications, 2005. ISDA '05. Proceedings. 5th](#)
[Conference on](#)
8-10 Sept. 2005 Page(s):356 - 361
Digital Object Identifier 10.1109/ISDA.2005.38
[AbstractPlus](#) | [Full Text: PDF\(200 KB\)](#) IEEE CNF
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- ☐ **23. Optimal design by resolving Boolean equations**
Steinbach, B.; Posthoff, C.;
[CAD Systems in Microelectronics, 2003. CADSM 2003. Proceedings of the 7th](#)
[Conference. The Experience of Designing and Application of](#)
18-22 Feb. 2003 Page(s):454 - 457
[AbstractPlus](#) | [Full Text: PDF\(341 KB\)](#) IEEE CNF
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- ☐ **24. Optimal design using implicitly given function sets**
Steinbach, B.; Posthoff, C.;
[CAD Systems in Microelectronics, 2003. CADSM 2003. Proceedings of the 7th](#)
[Conference. The Experience of Designing and Application of](#)
18-22 Feb. 2003 Page(s):446 - 453
[AbstractPlus](#) | [Full Text: PDF\(501 KB\)](#) IEEE CNF
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- ☐ **25. A semantics of UML state-machines using synchronous pre-order transit**
Yunming Wang; Talpin, J.-P.; Benveniste, A.; Le Guernic, P.;
[Object-Oriented Real-Time Distributed Computing, 2000. \(ISORC 2000\) Proce](#)
[IEEE International Symposium on](#)
15-17 March 2000 Page(s):96 - 103
Digital Object Identifier 10.1109/ISORC.2000.839516
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1 [On randomization in sequential and distributed algorithms](#)



Rajiv Gupta, Scott A. Smolka, Shaji Bhaskar

March 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 1

Publisher: ACM Press

Full text available: [pdf\(8.01 MB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Probabilistic, or randomized, algorithms are fast becoming as commonplace as conventional deterministic algorithms. This survey presents five techniques that have been widely used in the design of randomized algorithms. These techniques are illustrated using 12 randomized algorithms—both sequential and distributed—that span a wide range of applications, including: primality testing (a classical problem in number theory), interactive probabilistic proofs ...

Keywords: Byzantine agreement, CSP, analysis of algorithms, computational complexity, dining philosophers problem, distributed algorithms, graph isomorphism, hashing, interactive probabilistic proof systems, leader election, message routing, nearest-neighbors problem, perfect hashing, primality testing, probabilistic techniques, randomized or probabilistic algorithms, randomized quicksort, sequential algorithms, transitive tournaments, universal hashing

2 [BDD variable ordering for interacting finite state machines](#)



Adnan Aziz, Serdar Tasiran, Robert K. Brayton

June 1994 **Proceedings of the 31st annual conference on Design automation DAC '94**

Publisher: ACM Press

Full text available: [pdf\(130.77 KB\)](#)Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

3 [Stream algorithms and complexity: Randomized computations on large data sets: tight lower bounds](#)



Martin Grohe, André Hernich, Nicole Schweikardt

June 2006 **Proceedings of the twenty-fifth ACM SIGMOD-SIGACT-SIGART symposium on Principles of database systems PODS '06**

Publisher: ACM Press

Full text available: [pdf\(286.21 KB\)](#)Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

We study the randomized version of a computation model (introduced in [9, 10]) that restricts random access to external memory and internal memory space. Essentially, this model can be viewed as a powerful version of a data stream model that puts no cost on sequential scans of external memory (as other models for data streams) and, in addition, (like other external memory models, but unlike streaming models), admits several large external memory devices that can be read and written to in parallel ...

Keywords: XML, complexity, data streams/real-time data, query processing/query optimization, semi-structured data

4 Two tapes are better than one for nondeterministic machines



Pavol Dûriš, Zvi Galil

May 1982 **Proceedings of the fourteenth annual ACM symposium on Theory of computing STOC '82**

Publisher: ACM Press

Full text available: pdf(465.11 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

It is known that k tapes are no better than two tapes for nondeterministic machines. We show here that two tapes are better than one. In fact, we show that two pushdown stores are better than one tape. Also, k tapes are no better than two for nondeterministic reversal-bounded machines. We show here that two tapes are better than one for such machines. In fact, we show that two reversal-bounded pushdown stores are better than one reversal-bounded tape. We also show that for one-tape nondeter ...

5 Reversal complexity of counter machines



Tat-hung Chan

May 1981 **Proceedings of the thirteenth annual ACM symposium on Theory of computing STOC '81**

Publisher: ACM Press

Full text available: pdf(1.05 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

It has long been known that deterministic 1-way counter machines recognize exactly all r.e. sets. Here we investigate counter machines with general recursive bounds on counter reversals. Our main result is that for bounds which are at least linear, counter reversal is polynomially related to Turing machine time, for both 1-way and 2-way counter machines and in both the deterministic and the nondeterministic cases. This leads to natural characterizations of the classes P and NP, and hence of ...

6 A logic programming approach to knowledge-state planning: Semantics and complexity



Thomas Eiter, Wolfgang Faber, Nicola Leone, Gerald Pfeifer, Axel Polleres

April 2004 **ACM Transactions on Computational Logic (TOCL)**, Volume 5 Issue 2

Publisher: ACM Press

Full text available: pdf(333.40 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

We propose a new declarative planning language, called K, which is based on principles and methods of logic programming. In this language, transitions between states of knowledge can be described, rather than transitions between completely described states of the world, which makes the language well suited for planning under incomplete knowledge. Furthermore, our formalism enables the use of default principles in the planning process by supporting negation as failure. Nonetheless, K also support ...

Keywords: Answer sets, computational complexity, conformant planning, declarative planning, incomplete information, knowledge-states, secure planning

7 Complexity and expressive power of logic programming



Evgeny Dantsin, Thomas Eiter, Georg Gottlob, Andrei Voronkov

September 2001 **ACM Computing Surveys (CSUR)**, Volume 33 Issue 3

Publisher: ACM Press

Full text available: pdf(552.99 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

This article surveys various complexity and expressiveness results on different forms of logic programming. The main focus is on decidable forms of logic programming, in particular, propositional logic programming and datalog, but we also mention general logic programming with function symbols. Next to classical results on plain logic programming (pure Horn clause programs), more recent results on various important extensions of logic programming are surveyed. These include logic programming with ...

Keywords: Complexity, datalog, expressive power, logic programming, nonmonotonic logic, query languages

8 Wireless and sensor: Computation in networks of passively mobile finite-state sensors



Dana Angluin, James Aspnes, Zoë Diamadi, Michael J. Fischer, René Peralta

July 2004 **Proceedings of the twenty-third annual ACM symposium on Principles of distributed computing PODC '04**

Publisher: ACM Press

Full text available: pdf(223.67 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We explore the computational power of networks of small resource-limited mobile agents. We define two new models of computation based on pairwise interactions of finite-state agents in populations of finite but unbounded size. With a fairness condition on interactions, we define the concept of stable computation of a function or predicate, and give protocols that stably compute functions in a class including Boolean combinations of threshold- k , parity, majority, and simple arithmetic. We ...

Keywords: diffuse computation, finite-state agent, intermittent communication, mobile agent, sensor net, stable computation

9 Using model checking to find serious file system errors



Junfeng Yang, Paul Twohey, Dawson Engler, Madanlal Musuvathi

November 2006 **ACM Transactions on Computer Systems (TOCS)**, Volume 24 Issue 4

Publisher: ACM Press

Full text available: pdf(534.00 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This article shows how to use model checking to find serious errors in file systems. Model checking is a formal verification technique tuned for finding corner-case errors by comprehensively exploring the state spaces defined by a system. File systems have two dynamics that make them attractive for such an approach. First, their errors are some of the most serious, since they can destroy persistent data and lead to unrecoverable corruption. Second, traditional testing needs an impractical, expon ...

Keywords: Model checking, crash, file system, journaling, recovery



Technical reports

SIGACT News Staff

January 1980 **ACM SIGACT News**, Volume 12 Issue 1

Publisher: ACM Press

Full text available: [pdf\(5.28 MB\)](#) Additional Information: [full citation](#)

11 The complexity of problems on probabilistic, nondeterministic, and alternating decision trees



Udi Manber, Martin Tompa

July 1985 **Journal of the ACM (JACM)**, Volume 32 Issue 3

Publisher: ACM Press

Full text available: [pdf\(1.08 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

This work generalizes decision trees in order to study lower bounds on the running times of algorithms that allow probabilistic, nondeterministic, or alternating control. It is shown that decision trees that are allowed internal randomization (at the expense of introducing a small probability of error) run no faster asymptotically than ordinary decision trees for a collection of natural problems. Two geometric techniques from the literature for proving lower bounds on the time required by o ...

12 Computing curricula 2001



September 2001 **Journal on Educational Resources in Computing (JERIC)**

Publisher: ACM Press

Full text available: [pdf\(613.63 KB\)](#) [html\(2.78 KB\)](#) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

13 Algebraic acceptance mechanisms for polynomial time machines



Ulrich Hertrampf

June 2000 **ACM SIGACT News**, Volume 31 Issue 2

Publisher: ACM Press

Full text available: [pdf\(863.81 KB\)](#) Additional Information: [full citation](#), [abstract](#), [index terms](#)

We present several results, mainly taken from the author's recent conference articles [Her97a, Her97b, Her99], whose common theme is the application of (quite basic) algebraic techniques to describe complexity classes, which appear in the many ways nondeterministic polynomial time machines can be used (or abused). One possibility to motivate this is an exact characterization of the (at first glance) rather weak (2 out of 3)-P-set paradigm (see the Introduction for a description), which we will pr ...

14 Models and languages for parallel computation



David B. Skillicorn, Domenico Talia

June 1998 **ACM Computing Surveys (CSUR)**, Volume 30 Issue 2

Publisher: ACM Press

Full text available: [pdf\(298.05 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

We survey parallel programming models and languages using six criteria to assess their suitability for realistic portable parallel programming. We argue that an ideal model should be easy to program, should have a software development methodology, should be architecture-independent, should be easy to understand, should guarantee performance, and should provide accurate information about the cost of programs. These criteria reflect

our belief that developments in parallelism must be driven b ...

Keywords: general-purpose parallel computation, logic programming languages, object-oriented languages, parallel programming languages, parallel programming models, software development methods, taxonomy

15 Symmetric Complementation



John H. Reif

March 1984 **Journal of the ACM (JACM)**, Volume 31 Issue 2

Publisher: ACM Press

Full text available: pdf(1.26 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

16 Model abstraction for formal verification

Y.-W. Hsieh, S. P. Levitan

February 1998 **Proceedings of the conference on Design, automation and test in Europe DATE '98**

Publisher: IEEE Computer Society

Full text available: pdf(201.59 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)
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As the complexity of circuit designs grows, designers look toward formal verification to achieve better test coverage for validating complex designs. However, this approach is inherently computationally intensive, and hence, only small designs can be verified using this method. To achieve better performance, model abstraction is necessary. Model abstraction reduces the number of states necessary to perform formal verification while maintaining the functionality of the original model with respect ...

17 Parallel molecular computation



John H. Reif

July 1995 **Proceedings of the seventh annual ACM symposium on Parallel algorithms and architectures SPAA '95**

Publisher: ACM Press

Full text available: pdf(1.75 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

18 Subject and classification-code indexes



February 1973 **Proceedings of the 1st annual computer science conference on Program information abstracts CWC '73**

Publisher: ACM Press

Full text available: pdf(3.19 MB) Additional Information: [full citation](#), [abstract](#)

These indexes were prepared by William S. Stalcup, Steven A. Holton and Anthony E. Petrarca, Department of Computer and Information Science, The Ohio State University with the aid of programs developed by W. Michael Lay as part of his Doctoral research. The technique used for production of these indexes is a variation of the Double-KWIC Coordinate Indexing Technique, various aspects of which have been described by A. E. Petrarca and W. M. Lay in [J. Chem. Doc.](#), 9(1969); & ...

19 Curriculum 68: Recommendations for academic programs in computer science: a report of the ACM curriculum committee on computer science



William F. Atchison, Samuel D. Conte, John W. Hamblen, Thomas E. Hull, Thomas A. Keenan, William B. Kehl, Edward J. McCluskey, Silvio O. Navarro, Werner C. Rheinboldt, Earl J.

Schweppe, William Viavant, David M. Young

March 1968 **Communications of the ACM**, Volume 11 Issue 3

Publisher: ACM Press

Full text available:  pdf(6.63 MB) Additional Information: [full citation](#), [references](#), [citations](#)

Keywords: computer science academic programs, computer science bibliographies, computer science courses, computer science curriculum, computer science education, computer science graduate programs, computer science undergraduate programs

20 Test sequence generation for controller verification and test with high coverage



Sezer Gören, F. Joel Ferguson

October 2006 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 11 Issue 4

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Verification and test are critical phases in the development of any hardware or software system. This article focuses on black box testing of the control part of hardware and software systems. Black box testing involves specification, test generation, and fault coverage. Finite state machines (FSMs) are commonly used for specifying controllers. FSMs may have shortcomings in modeling complex systems. With the introduction of X-machines, complex systems can be modeled at higher levels of abstracti ...

Keywords: Fault coverage, X-machine, black box testing, finite state machine

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